

PE55J Series 3.3 V PECL Clock Oscillators

April 2017



- Pletronics' PE55J Series is a quartz crystal controlled precision square wave generator with a PECL output.
- Improved phase noise performance.
- Low cost mass produced oscillator.
- Tape and Reel or cut tape packaging is available.
- 3.2 x 5 mm LCC Ceramic Package
- Enable/Disable Function on pad 1
- Disable function includes low standby power mode
- Fundamental and 3rd Overtone Crystals used
- Improved circuit to minimize oscillator issues such as multi-mode output signal.
- Low Jitter Product

100 fS Jitter 12.0 KHz to 20.0 MHz @ 156.25 MHz

**Pletronics Inc. certifies this device is in accordance with the
RoHS 6/6 (2011/65/EC) and WEEE (2002/96/EC) directives.**

Pletronics Inc. guarantees the device does not contain the following:
Cadmium, Hexavalent Chromium, Lead, Mercury, PBB's, PBDE's
Weight of the Device: 0.09 grams
Moisture Sensitivity Level: 1 As defined in J-STD-020D.1
Second Level Interconnect code: e4

Absolute Maximum Ratings:

Parameter	Unit
V _{CC} Supply Voltage	-0.3V to +4.0V
V _i Input Voltage	-0.3V to V _{CC} + 0.3V
V _o Output Voltage	-0.3V to V _{CC} + 0.3V

Thermal Characteristics

The maximum die or junction temperature is 125°C
The thermal resistance junction to board is 30 to 50°C/Watt depending on the solder pads, ground plane and construction of the PCB.

Part Number:

PE55	45	J	E	V	-125.0M	-XX
Packaging code or blank T250 = 250 per Tape and Reel T500 = 500 per Tape and Reel T1K = 1000 per Tape and Reel						
Frequency in MHz						
Supply Voltage V_{CC} V = 3.3V ± 10%						
Optional Enhanced OTR Blank = Temp. range -10 to +70°C C = Temp. range -20 to +70°C E = Temp. range -40 to +85°C						
Series Model						
Frequency Stability 45 = ± 50 ppm 44 = ± 25 ppm 20 = ± 20 ppm						
Series Model						

Available Frequencies:

- 100.0MHz
 - 106.25MHz
 - 125.0MHz
 - 133.0MHz
 - 156.25MHz
- Contact factory for other options**

Part Marking:

PFF.FFP
• **YMDX**

Marking Legend:

- P = Pletronics and PECL
- FF.FF = Frequency in MHz
- YMD = Date of Manufacture (year-month-day)
- All other marking is internal factory codes

Specifications such as frequency stability, supply voltage and operating temperature range, etc. are not identified from the marking. External packaging labels and packing list will correctly identify the ordered Pletronics part number.

Codes for Date Code YMD

Code	4	5	6	7	8	Code	A	B	C	D	E	F	G	H	J	K	L	M
Year	2014	2015	2016	2017	2018	Month	JAN	FEB	MAR	APR	MAY	JUN	JUL	AUG	SEP	OCT	NOV	DEC

Code	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F	G
Day	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16
Code	H	J	K	L	M	N	P	R	T	U	V	W	X	Y	Z	
Day	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31	

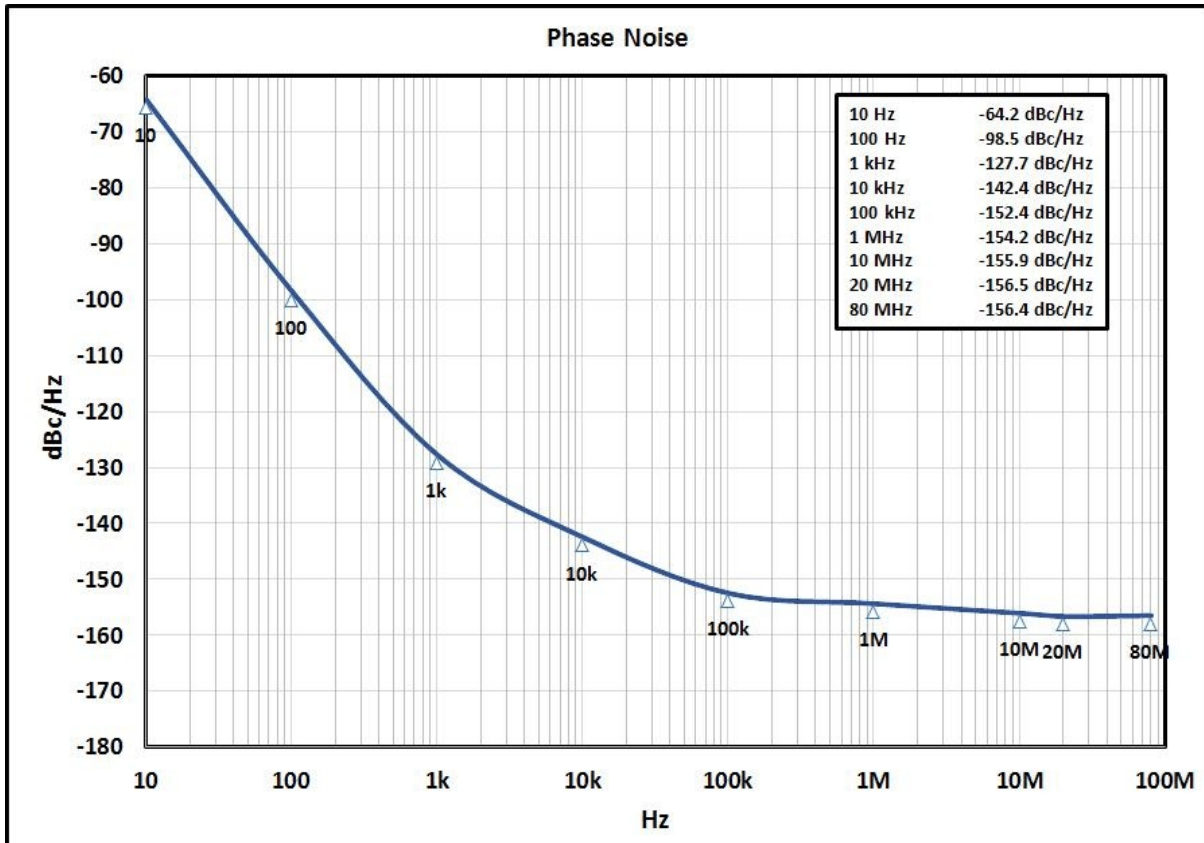
Electrical Specification for 3.30V $\pm 5\%$ over the specified temperature range and the frequency range of 25 to 175 MHz

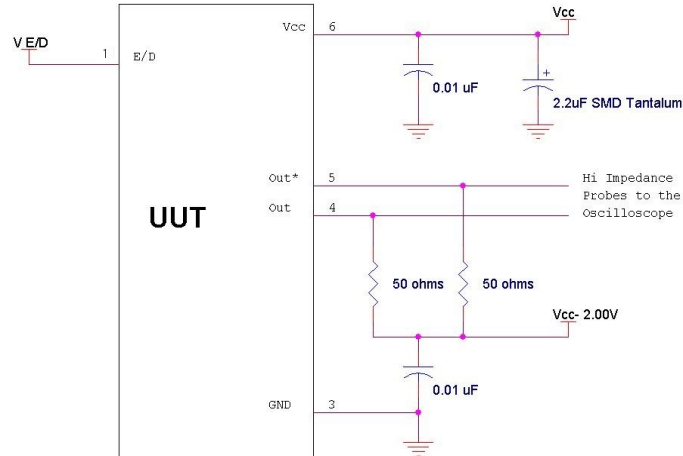
Item	Min	Typ	Max	Unit	Condition
Frequency Accuracy "45"	-50	-	+50	ppm	For all supply voltages, load changes, aging for 1 year, shock, vibration and temperatures
"44"	-25	-	+25		
"20"	-20	-	+20		
Output Waveform	PECL /ECL				
Output High Level	2.275	2.35	2.420	V	$V_{CC} = 3.3 V$
Output Low Level	1.490	1.60	1.680	V	$V_{CC} = 3.3 V$
Output Symmetry	45	-	55	%	at 50% point of V_{CC} (See load circuit)
Jitter ¹	-	0.1	-	pS RMS	12 KHz to 20 MHz from the output frequency
	-	1.25	-	pS RMS	10 Hz to 1 MHz from the output frequency
Output T_{RISE} and T_{FALL}	-	-	0.5	nS	Vth is 20% and 80% of waveform
V_{CC} Supply Current (I_{CC})	-	-	60	mA	
Enable/Disable Internal Pull-up	50	-	-	Kohm	to V_{CC} , measured with Pad 1 = 0.0 volts
V disable	-	-	0.7	V	Referenced to pad 3, $0.3 V_{CC}$
V enable	1.7	-	-	V	Referenced to pad 3, $0.7 V_{CC}$
Output leakage $V_{OUT} = V_{CC}$	-10	-	+10	uA	Pad 1 low, device disabled
	$V_{OUT} = 0V$	-10	+10	uA	
Enable time	-	-	2	mS	Time for output to reach a logic state, the output frequency is correct at the specified Start Time.
Disable time	-	-	200	nS	Time for output to reach a high Z state
Start up time	-	-	10	mS	Time for output to reach specified frequency
Operating Temperature Range	-10	-	+70	°C	Standard Temperature Range
	-20	-	+70	°C	Extended Temperature Range "C" Option
	-40	-	+85	°C	Extended Temperature Range "E" Option
Storage Temperature Range	-55	-	+125	°C	
Standby Current I_{CC}	-	-	30	uA	Pad 1 low, device disabled

¹ Jitter computed from phase noise data at 156.25MHz

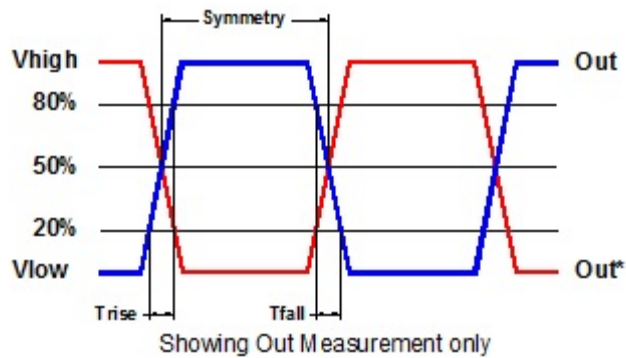
Specifications with Pad 1 E/D open circuit unless stated otherwise

Typical Phase Noise





Load Circuit



Test Waveform

Reliability: Environmental Compliance

Parameter	Condition
Mechanical Shock	MIL-STD-883 Method 2002, Condition B
Vibration	MIL-STD-883 Method 2007, Condition A
Solderability	MIL-STD-883 Method 2003
Thermal Shock	MIL-STD-883 Method 1011, Condition A





ESD Rating

Model	Minimum Voltage	Conditions
Human Body Model	1500	MIL-STD-883 Method 3115
Charged Device Model	1000	JESD 22-C101

Package Labeling

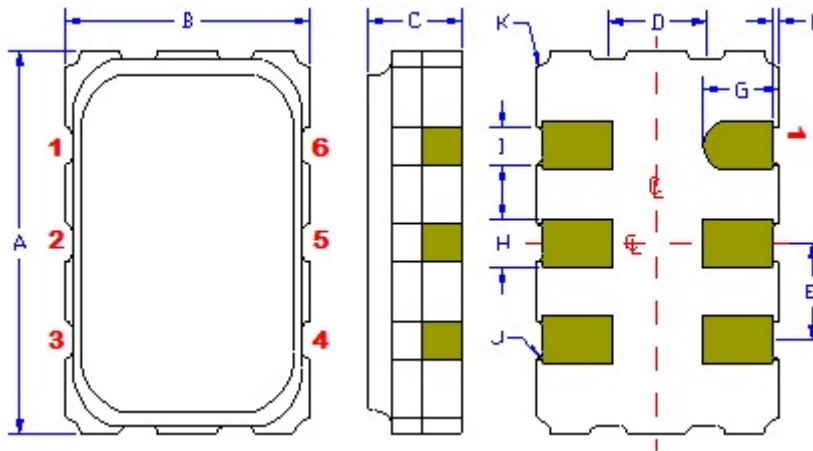
Label is 1" x 2.6" (25.4mm x 66.7mm)
Font is Courier New
Bar code is 39-Full ASCII

Label is 1" x 2.6" (25.4mm x 66.7mm)
Font is Arial

P/N:  PE5545JV-100.0M Customer P/N:  12345678 Qty:  1000 D/C  0JX-MTG MSL: 1

RoHS Compliant 2nd Lvl Interconnect Category=e4 Max Safe Temp=260C for 10s 2X Max

Mechanical:



	Inches	mm
A	0.197 \pm 0.006	5.00 \pm 0.15
B	0.125 \pm 0.006	3.20 \pm 0.15
C	0.053 max	1.35 max
D ¹	0.050	1.27
E ¹	0.050	1.27
F ¹	0.004	0.10
G ¹	0.039	1.00
H ¹	0.025	0.63
I ¹	0.020	0.50
J ¹	0.004R	0.10R
K ¹	0.008R	0.20R

Contacts:

Gold 11.8 to 39.4 μ mches (0.3 to 1.0 μ m)
over
Nickel 50 to 350 μ mches (1.27 to 8.89 μ m)

¹ Typical dimensions

Not to Scale

Pad	Function	Note
1	Output Enable/Disable	When this pad is not connected the oscillator shall operate. When this pad is <0.30 volts, the output will be inhibited (high impedance state.) Recommend connecting this pad to V _{CC} if the oscillator is to be always on.
2	No connect	There is no internal connection to this pad
3	Ground (GND)	
4	Output	Both outputs must be terminated and biased for proper operation. The ideal termination is 50 ohms connected to 2.0V below the Supply Voltage.
5	Output*	
6	Supply Voltage (V _{CC})	Recommend connecting appropriate power supply bypass capacitors as close as possible.

Layout and application information

Recommend connecting Pad 1 and Pad 2 together to permit the design to accept Enable/Disable input on either pad

For Optimum Jitter Performance, Pletronics recommends:

- a ground plane under the device
- no large transient signals (both current and voltage) should be routed under the device
- do not layout near a large magnetic field such as a high frequency switching power supply
- do not place near piezoelectric buzzers or mechanical fans.

IMPORTANT NOTICE

Pletronics Incorporated (PLE) reserves the right to make corrections, improvements, modifications and other changes to this product at anytime. PLE reserves the right to discontinue any product or service without notice. Customers are responsible for obtaining the latest relevant information before placing orders and should verify that such information is current and complete. All products are sold subject to PLE's terms and conditions of sale supplied at the time of order acknowledgment.

PLE warrants performance of this product to the specifications applicable at the time of sale in accordance with PLE's limited warranty. Testing and other quality control techniques are used to the extent PLE deems necessary to support this warranty. Except where mandated by specific contractual documents, testing of all parameters of each product is not necessarily performed.

PLE assumes no liability for application assistance or customer product design. Customers are responsible for their products and applications using PLE components. To minimize the risks associated with the customer products and applications, customers should provide adequate design and operating safeguards.

PLE products are not designed, intended, authorized or warranted to be suitable for use in life support applications, devices or systems or other critical applications that may involve potential risks of death, personal injury or severe property or environmental damage. Inclusion of PLE products in such applications is understood to be fully at the risk of the customer. Use of PLE products in such applications requires the written approval of an appropriate PLE officer. Questions concerning potential risk applications should be directed to PLE.

PLE does not warrant or represent that any license, either express or implied, is granted under any PLE patent right, copyright, artwork or other intellectual property right relating to any combination, machine or process which PLE product or services are used. Information published by PLE regarding third-party products or services does not constitute a license from PLE to use such products or services or a warranty or endorsement thereof. Use of such information may require a license from a third party under the patents or other intellectual property of the third party, or a license from PLE under the patents or other intellectual property of PLE.

Reproduction of information in PLE data sheets or web site is permissible only if the reproduction is without alteration and is accompanied by associated warranties, conditions, limitations and notices. Reproduction of this information with alteration is an unfair and deceptive business practice. PLE is not responsible or liable for such altered documents.

Resale of PLE products or services with statements different from or beyond the parameters stated by PLE for that product or service voids all express and implied warranties for the associated PLE product or service and is an unfair or deceptive business practice. PLE is not responsible for any such statements.

Contacting Pletronics Inc.

Pletronics Inc.
19013 36th Ave. West
Lynnwood, WA 98036-5761 USA

Tel: 425-776-1880
Fax: 425-776-2760
E-mail: ple-sales@pletronics.com
URL: www.pletronics.com

Copyright © 2006, 2007, 2010, 2011 Pletronics Inc.

Mouser Electronics

Authorized Distributor

Click to View Pricing, Inventory, Delivery & Lifecycle Information:

[Pletronics:](#)

[PE5545JEV-156.25M](#) [PE5545JEV-100.0M](#) [PE5545JEV-150.0M](#) [PE5545JEV-125.0M](#)